



12/28/99

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

5/16/00

U.S. **UTILITY** Patent Application

O.I.P.E. HT PATENT DATE
SCANNED Q.A.

APPLICATION NO. 09/473448	CONT/PRIOR	CLASS 712 703	SUBCLASS 228	ART UNIT 2183 2123	EXAMINER Coleman Chan, Eileen
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TITLE: APPLICANTS

George Thangadurai
Erik Lode

Novel way to save processor architectural state for later process resumption

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)			
	_____ (Date)		ISSUE FEE	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
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